

PATENT APPLICATION Docket No. 8371-129

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Apostolos Voutsas and Yukihiko Nakata

Serial No. 09/945,063 Examiner: George R. Fourson III

Filed: August 31, 2001 Group Art Unit: 2823

For: METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR

(TFT) AND THE DEVICE FORMED THEREBY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Responsive to the Office Action, dated November 19, 2003, please amend the application as follows.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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